

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of: Marcus A. BAKER et al.

Serial No.: To Be Assigned

Filed: Herewith

Title: METHOD AND SYSTEM FOR SAVING THE STATE OF INTEGRATED CIRCUITS UPON FAILURE

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT (IDS) UNDER 37 CFR 1.97

Dear Sir:

Applicants submit herewith patents, publications or other information which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 CFR 1.98(a)(1).

A copy of each of the items on PTO-1449 is supplied herewith.

Respectfully submitted,

04/30/2003
Date

By:


Michele Liu
Sawyer Law Group LLP
Attorney for Applicant(s)
Registration No. 44,875

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| FORM PTO-1449 (Modified) | | ATTY. DOCKET NO. RPS920030073US1 | SERIAL NO. To Be Assigned |
| LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT | | APPLICANT: Marcus A. BAKER et al. | |
| (Use several sheets if necessary) | | FILING DATE: Herewith | GROUP: To Be Assigned |

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

| EXAMINER INITIALS | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE (IF APPRO.) |
|-------------------|-----------------|----------|-----------------|-------|----------|-------------------------|
| | 5 6 1 9 6 4 4 | 04/08/97 | Crockett et al. | 395 | 183.21 | |
| | 5 6 3 0 0 4 8 | 05/13/97 | La Joie et al. | 395 | 183.01 | |
| | 5 9 2 8 3 6 8 | 07/27/99 | Jardine et al. | 714 | 22 | |
| | 5 9 3 3 5 9 4 | 08/03/99 | La Joie et al. | 395 | 183.01 | |
| | 6 1 4 8 3 4 8 | 11/14/00 | Garnett et al. | 710 | 14 | |
| | 6 2 0 2 1 7 4 | 03/13/01 | Lee et al. | 714 | 38 | |

FOREIGN PATENT DOCUMENTS

| | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | Translation |
|--|-----------------|----------|---------|-------|----------|-------------|
| | 4 0 2 5 9 4 1 | 01/29/92 | Japan | | | Yes |
| | | | | | | |

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

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| | | Bowen, Nicholas S. et al., "Virtual Checkpoints: Architecture and Performance," IEEE Transactions on Computers, Vol. 41, No. 5, May 1992, pp. 516-525. |
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REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

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|-------------------|-----------------|---|---|---|---|---|---|----------|-----------------|-------|----------|-------------------------|
| | 6 | 2 | 4 | 3 | 8 | 3 | 1 | 06/05/01 | Mustafa et al. | 714 | 24 | |
| | 6 | 3 | 9 | 3 | 5 | 8 | 2 | 05/21/02 | Klecka et al. | 714 | 11 | |
| | 6 | 5 | 1 | 6 | 4 | 3 | 4 | 02/04/03 | Willekes et al. | 714 | 738 | |
| | 6 | 5 | 3 | 9 | 3 | 4 | 1 | 03/25/03 | Li et al. | 702 | 187 | |
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FOREIGN PATENT DOCUMENTS

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OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

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